



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

In re Application of:

Pendharker et al.

Atty. Docket: TI-33654

Serial No.: 10/678,028

Art Unit: TBD

Filed: 09/29/2003

Examiner: TBD

**For: A LOW NOISE VERTICAL VARIABLE GATE CONTROL  
VOLTAGE JFET DEVICE IN A BICMOS PROCESS AND  
METHODS TO BUILD THIS DEVICE**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

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Jackie McBride  
Jackie McBride

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO-1449. Copies of the noted documents are enclosed. Please charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. Charge any additional fee required or credit any overpayment to the same.

Texas Instruments Incorporated  
P. O. Box 655474, M/S 3999  
Dallas, Texas 75265  
(972) 917-5355

Respectfully submitted,

Yingsheng Tung  
Yingsheng Tung  
Attorney for Applicant(s)  
Reg. No. 52,305

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.